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Summary:

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ARM Architecture Reference Manual - ERNET The purpose of this manual is to describe the ARM instruction set architecture, including its high code density Thumb subset, and two of its standard coprocessor extensions: â€¢ The standard System Control coprocessor (coprocessor 15), which is used to control memory system. ARMv7-M Architecture Reference Manual This ARM Architecture Reference Manual may include technical inaccuracies or typographical errors. This document may be translated into other languages for convenience, and you agree that if there is any conflict between the ... ARMv7-M Architecture Reference Manual Preface.

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